IN THE CLAIMS:

Please amend the claims as follows:

1. (currently amended) Circuitry for providing data security, which circuitry contains at least one processor, ~~and~~ at least one storage circuit, authentication means arranged to authenticate software provided to the circuitry, and which circuitry comprises:

at least one storage area in said storage circuit, in which storage area protected data relating to circuitry security and protected applications are located;

mode setting means arranged to set said processor in one of at least two different operating modes, the mode setting means being capable of altering the processor operating mode;

storage circuit access control means arranged to enable said processor to access said storage area in which said protected data are located when a first processor operating mode is set, wherein only authenticated software and protected applications have access to said protected data; and

storage circuit access control means arranged to prevent said processor from accessing said storage area in which protected data are located when a second processor operating mode is set, thereby enabling said at least one processor to execute non-verified software downloaded into the circuitry.

2. (original) The circuitry for providing data security according to claim 1, further comprising:

a timer arranged to control a time period during which the processor is in said second operating mode.

3. (Cancelled)

4. (original) The circuitry for providing data security according to claim 1, further comprising:

means arranged to indicate in which mode the processor is operating.

5. (original)   The circuitry for providing data security according to claim 1, wherein said mode setting means comprise an application program.

6. (original)   The circuitry for providing data security according to claim 1, which circuitry is comprised in a mobile telecommunication terminal.

7. (currently amended)   A method, comprising:

storing protected data relating to ~~circuitry~~ security of circuitry and protected applications in a storage circuit;

authenticating software provided to the circuitry;

setting a processor in one of at least two different alterable operating modes;

enabling said processor to access said storage area in which said protected data are located when a first processor operating mode is set, wherein only authenticated software and protected applications have access to said protected data; and

preventing said processor from accessing said storage area in which protected data are located when a second processor operating mode is set, thereby enabling said at least one processor to execute non-verified software downloaded into the circuitry.

8. (previously presented)   The method for providing data security according to claim 7, further comprising:

controlling a time period during which the processor is in said second operating mode by means of a timer.

9. (Cancelled)

10. (previously presented) The method for providing data security according to claim 7, further comprising:

indicating in which mode the processor is operating.

11. (original) The method for providing data security according to claim 7, wherein the setting of said processor in one of at least two different alterable operating modes is performed by means of an application program.

12. (original) The method for providing data security according to claim 7, wherein the circuitry containing at least one processor and at least one storage circuit is comprised in a mobile telecommunication terminal.

13. (currently amended) Data security circuitry ~~Circuitry~~ for providing data security, which data security circuitry contains at least one processor,~~and~~ at least one storage circuit, authentication circuitry arranged to authenticate software provided to the data security circuitry and which data security circuitry comprises:

at least one storage area in said storage circuit, in which storage area protected data relating to data security circuitry security and protected applications are located;

mode setting circuitry arranged to set said processor in one of at least two different operating modes, the mode setting circuitry being capable of altering the processor operating mode;

storage circuit access control circuitry arranged to enable said processor to access said storage area in which said protected data are located when a first processor operating mode is set, wherein only authenticated software and protected applications have access to said protected data; and

storage circuit access control circuitry arranged to prevent said processor from accessing said storage area in which protected data are located when a second processor operating mode is set, thereby enabling said at least one processor to execute non-verified software downloaded into the data security circuitry for providing data security.

14. (currently amended) The data security circuitry for providing data security according to claim 13, further comprising:

a timer arranged to control a time period during which the processor is in said second operating mode.

15. (Cancelled)

16. (currently amended) The data security circuitry for providing data security according to claim 13, further comprising:

indicator circuitry arranged to indicate in which mode the processor is operating.

17. (currently amended) The data security circuitry for providing data security according to claim 13, wherein said mode setting circuitry comprises an application program.

18. (currently amended) The data security circuitry for providing data security according to claim 13, which circuitry is comprised in a mobile telecommunication terminal.